


**Electronic Filing System (EFS) Data**  
**Electronic Patent Application Submission**  
**USPTO Use Only**

EFS ID: 58690  
Application ID: 10709048   
Title of Invention: A MANUFACTURABLE METHOD  
AND STRUCTURE FOR DOUBLE  
SPACER CMOS WITH OPTIMIZED  
NFET/PFET PERFORMANCE  
First Named Inventor: Sadanand Deshpande  
Domestic/Foreign Application: Domestic Application  
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U.S. DEPARTMENT OF COMMERCE  
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FEE RECORD SHEET

04/09/2004 HLE333 00000067 090458 10709048

01 FC:1001	770.00 DA
02 FC:1201	86.00 DA
03 FC:1202	90.00 DA